

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	17480	(micro adj2 mirror\$1) micro-mirror\$1 micromirror\$1	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 13:47
L2	264335	(semiconduct\$3 semi- conduct\$3) adj2 substrate\$1	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 13:47
L3	925363	circuit\$2 near3 control \$4	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 13:48
L4	3577405	support\$3	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 13:48
L5	15	1 same 2 same 3 same 4	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 13:48
L6	4905	2 same 3	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:02
L7	165	2 same 3 same 4	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:03
L8	21	1 and 7	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:03
L9	6	8 not 5	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:03
L10	41	MEMS and 7	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:05
L11	20	10 not 8	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:05

L12	645	circuit\$2 with ((bottom second\$3) adj2 surface \$1) with 2	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:12
L13	0	1 same 12	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:13
L14	0	1 and 12	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:13
L15	13	MEMS and 12	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:13
L16	13	15 not 10	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:13
L17	24	4 same 12	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:15
L19	24	17 not 16	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:15
L20	1	"20040125347"	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:40
L21	1	(silicon same oxide) and 20	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 14:40
L22	1	("6639/13").PN.	US- PGPUB; USPAT	OR	OFF	2008/12/02 15:00
L23	384	circuitry with (epitaxial adj2 layer\$1)	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 15:07
L24	0	1 same 23	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 15:07

L25	6	1 and 23	US- PGPUB; USPAT; EPO; JPO	OR	ON	2008/12/02 15:07
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